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(71) Applicant (for all designated States except US): **KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]**;
Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **LETAVIC, Theodore**
[US/US]; 1109 McKay Drive, M/S-41SJ, San Jose, CA
95131 (US).

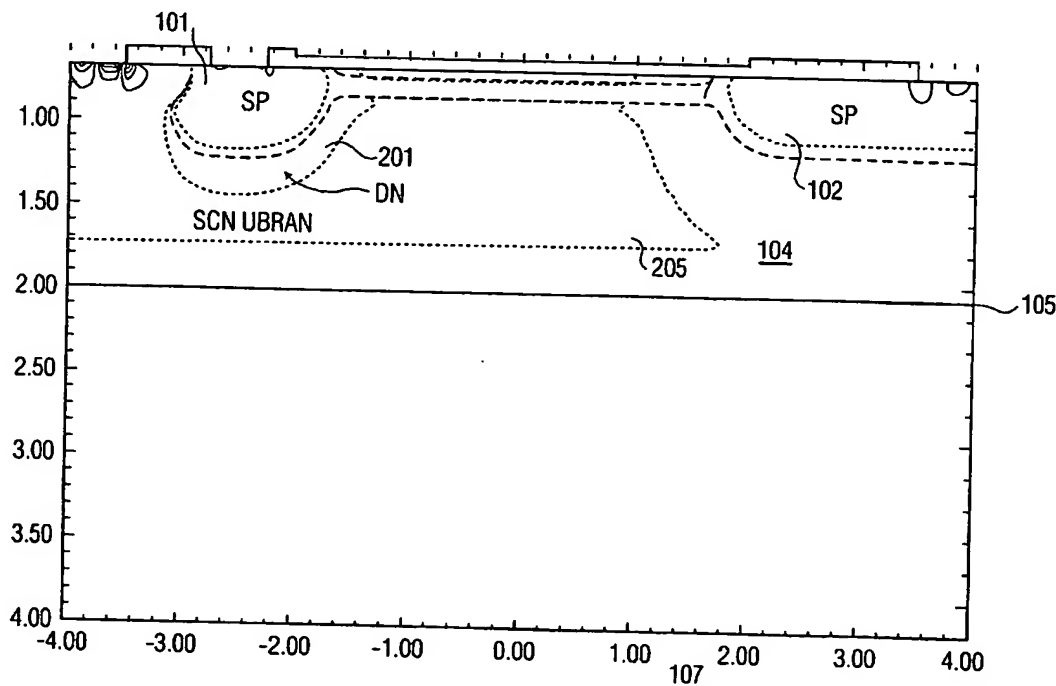
(74) Agent: **KONINKLIJKE PHILIPS ELECTRONICS N.V.**; c/o LESTER, Shannon, 1109 McKay Drive,
M/S-41SJ, San Jose, CA 95131 (US).

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(54) Title: PREVENTION OF PARASITIC CHANNEL IN AN INTEGRATED SOI PROCESS



(57) Abstract: A Silicon on Insulator device is disclosed wherein a parasitic channel (110) induced in a thin film portion of the device is prevented from allowing current flow between the source (101) and drain (101) by a Deep N implant directly below the source (101) and the drain (101) that would otherwise occur.

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